

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (canceled)

Claim 2 (currently amended) A method of processing at least one channel input signal comprising the steps of:

receiving the input signal;

modifying the input signal to produce a second signal;

variably controlling ~~one of the input and second~~ signals; and

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mixing the variably controlled input signal and the ~~not variably controlled~~ second signal to produce variably controllable third, fourth and fifth channel output signals.

Claim 3 (currently amended): A circuit for processing at least one channel input signal comprising:

means for receiving the input signal;

means for modifying said received signal to produce a second signal;

means for variably controlling ~~at least one of said input and second~~ signals; and

means for mixing said variably controlled input signal and the second ~~not variably controlled~~ signal to produce variably controllable third, fourth and fifth channel output signals.

Claim 4 (previously presented): A method of processing at least one channel input signal comprising the steps of:

receiving the input signal;
modifying the input signal to produce a second signal;
variably controlling the input and second signals; and
mixing the variably controlled signals to produce variably controllable third, fourth and fifth channel output signals.

Claim 5 (previously presented): A circuit for processing at least one channel input signal comprising:

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means for receiving the input signal;
means for modifying said received signal to produce a second signal;
means for variably controlling said input and second signals; and
means for mixing said variably controlled signals to produce variably controllable third, fourth and fifth channel output signals.

Claim 6 (new): A method of processing at least one channel input signal comprising the steps of:

receiving the input signal;
modifying the input signal to produce a second signal;
variably controlling the second signal; and
mixing the variably controlled second signal and the input signal to produce variably controllable third, fourth and fifth channel output signals.

Claim 7 (new): A circuit for processing at least one channel input signal comprising:

means for receiving the input signal;

means for modifying said received signal to produce a second signal;

means for variably controlling the second signals; and

means for mixing said variably controlled second signal and the input signal to produce
variably controllable third, fourth and fifth channel output signals.
